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MAZIDI (CH 9 /"Part
1 /" AVR TIMER
PROGRAMMING IN
ASSEMBLY AND C)

Lab Update - Tron
Club Advanced Kit
#1, Challenge

Problem - AVR, LCD,
Shift Register. (#9)

~~Atmel Studio 7 (AVR
Studio) Tutorial: How
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Build New Project~~

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I2C bus for both RTC
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Arduino Shield

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burn/program a hex

file to a AVR

microcontroller using

AVR Studio,USBasp,B

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optimize the device
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Architecture– 133

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– Most Single Clock

Cycle Execution– 32

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Registers– Fully

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MHz– On-chip

2-cycle Multiplier•

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volatile Memory
segments– 128 ...

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348151G-AVR-07

/10ATmega128Ato

one enables the wait-

state and one extra

cycle is added during

read/write strobe as

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shown in Figure 28a

7-7.7.6.5 XMCRA -

External Memory

Control Register A •

Bit 7 – Res: Reserved

Bit This is a reserved bit and will always read as zero.

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Data RD Memory
access instruction ®
® AVR ATmega128A
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memory organized as
a contains a detailed
description on
EEPROM

programming is likely
to rise or fall slowly
on Power-up/down.
This causes the
device for some CC
the EEPROM data can
be corrupted because
the supply voltage is
CC, ... Page 22

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ATMEGA128A-AU

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The low-power Atmel 8-bit AVR RISC-based microcontroller combines 8KB of programmable flash memory, 1KB of SRAM, 512B

EEPROM, and a 6 or 8 channel 10-bit A/D converter. The device supports throughput

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of 16 MIPS at 16
MHz and operates
between 2.7-5.5 volts.

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Most of us consider the hardware in the chip to be the "library" and the datasheet for the chip, the instructions on using the library. If you don't know my whole story, keep your mouth shut. If you know my whole story, you're an accomplice.

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Serial Peripheral

Interface SPI library

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Microcontroller with

16K Bytes In-System

ProgrammableFlash,

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LCSC Part # C6510:

Package:

TQFP-64_14x14x08P

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Model. Description:

8-Bit FLASH AVR

16MHz 2.7V ~ 5.5V

TQFP-64_14x14x08P

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Microchip 128a

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What really confused me (being new to AVR) was that the ISP layout had SCK pin in it, but I couldn't find one while looking at the PDIP schematic. It wasn't until I looked at the MLF layout and

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noticed that SCK was there. I gave it a shot and hooked the ISP to the pin labeled SCL(on the PDIP schematic) and it worked.

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